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Milpitas, CA 95035 1621 Barber Lane, MS D-106 Legal Departm nt - 1P LSI Logic Corporation

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Semiconductor Wafer For: Probing Fixture and Method for Atty. Docket No.: 00-103 Filed: December 6, 2000 Examiner: Yennhu B. Huynh Serial No.: 09/731,596 Group Art Unit: 2813 In re Application of: Margaret S. Fyfield

## Restriction/Election Requirement RESPONSE TO OFFICIAL ACTION

Washington, D.C. 20231 Hon. Commissioner of Patents and Trademarks

:nS

without traverse, to prosecute Group J. i.e., method claims 1-15. the Examiner required restriction pursuant to 35 U.S.C. §121. Election is hereby made, This response is presented to the Office Action mailed January 7, 2004, wherein

Please withdraw claims 16-20 (i.e., Group II) from consideration.

Respectfully Submitted,

Margaret S. Fyfield

Milpitas, CA 95035-7458 W\S D-109 1621 Barber Lane LSI LOGIC CORPORATION (408) 433-7625 Reg. No. 36,771 by Timothy R. Croll Date: January 20, 2004